

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231 www.uspto.gov

ATTORNEY DOCKET NO. APPLICATION NO. FILING DATE FIRST NAMED INVENTOR CONFIRMATION NO. 09/601,327 08/08/2000 Agnes Bidard 194339US 2254

08/07/2002 OBLON SPIVAK MCCLELLAND MAIER & NEUSTADT PC FOURTH FLOOR 1755 JEFFERSON DAVIS HIGHWAY ARLINGTON, VA 22202

EXAMINER

BUDD, MARK OSBORNE PAPER NUMBER ART UNIT

2834

DATE MAILED: 08/07/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application 60	№. 327	Applicant(s)	Bidard		
Office Action Summary	Examiner	M.B.	8)	Group Art Unit 2834		
-The MAILING DATE of this communication appears	s on the co	er sheet b	eneath the co	rrespondence a	ddress	
Period for Reply		· ~				
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO OF THIS COMMUNICATION.	EXPIRE_		MONTH(S)	FROM THE MA	LING DATE	
 Extensions of time may be available under the provisions of 37 CFR 1. from the mailing date of this communication. If the period for reply specified above is less than thirty (30) days, a replied in NO period for reply is specified above, such period shall, by default, efficient to reply within the set or extended period for reply will, by statute 	ly within the st expire SIX (6) I	atutory minim MONTHS fron	um of thirty (30)	days will be conside of this communicat	red timely. ion .	
Status						
Responsive to communication(s) filed on 6-38-	03				•	
☐ This action is FINAL.						
☐ Since this application is in condition for allowance except for accordance with the practice under <i>Ex parte Quayle</i> , 1935				the merits is clo	sed in	
Disposition of Claims						
(Claim(s) 1-6				_ is/are pending in the application.		
Of the above claim(s)				is/are withdrawn from consideration.		
☐ Claim(s)	·		is/are a	llowed.		
Claim(s) 1-6			is/are r	is/are rejected.		
☐ Claim(s)						
	are subject to restriction or election requirement.					
Application Papers						
☐ See the attached Notice of Draftsperson's Patent Drawing						
☐ The proposed drawing correction, filed on		-	∐ disapproved	1.		
☐ The drawing(s) filed on is/are objected.	ea to by the	Examiner.				
 ☐ The specification is objected to by the Examiner. ☐ The oath or declaration is objected to by the Examiner. 						
Priority under 35 U.S.C. § 119 (a)-(d)						
 □ Acknowledgment is made of a claim for foreign priority und □ All □ Some* □ None of the CERTIFIED copies of the 		. , ,	• •			
received.	io priority de	coments ne	ive been			
☐ received in Application No. (Series Code/Serial Number	r)			·		
$\ \square$ received in this national stage application from the Inter				•		
*Certified copies not received:						
Attachment(s)						
☐ Information Disclosure Statement(s), PTO-1449, Paper No	lo(s) 🗆 Interview S			ummary, PTO-413		
□ Notice of Reference(s) Cited, PTO-892 □ Notice				of Informal Patent Application, PTO-152		
☐ Notice of Draftsperson's Patent Drawing Review, PTO-948	;		Other			
Office	Action Sum	nmary				

Application/Control Number: 09/601,327

Art Unit: 2834

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Onishi(142), Tsuji or Onishi(368) in view of Takoshima.

Each of Onishi(142) (figs. 16 and 18), Tsuji (figs. 1-9) and Onishi(368) figs. 1-5) teach a piezoelectric SAW coupled to a printed circuit board thru a "first" layer and conductive vias piercing the first layer and conductive vias piercing the first layer and the printed circuit board. In Onishi (142) note the first layer would be the adhesive #7 that surrounds conductor #6 which is part of conductive via #124. In Onishi (368), again note first layer #7 which surrounds the conductive elements that are part of the vias that pierce the printed circuit board. In Tsuji, first layer #13 surrounds the conductor elements that electrically are part of the conductive vias that penetrate the printed circuit board. While it is true the conductors of Onishi and Tsuji are not constructed of a single element, it is noted that making parts integral or separable has long been held to be within the skill expected of the routineer. Onishi and Tsuji do not show the printed circuit board having the same surface dimensions as the SAW device. However, Takoshima teaches making the printed board with the same dimensions as the SAW to save space and eliminate the need for a further housing cover: using the flip-side of the SAW as an exterior

Art Unit: 2834

cover. Thus for at least these reasons it would have been obvious to one of ordinary skill in the art to provide Onishi or Tsuji with the PCB having dimensions as these of the SAW.

Budd/ek

08/06/02

ART FRUT 212